

FIG. 1A

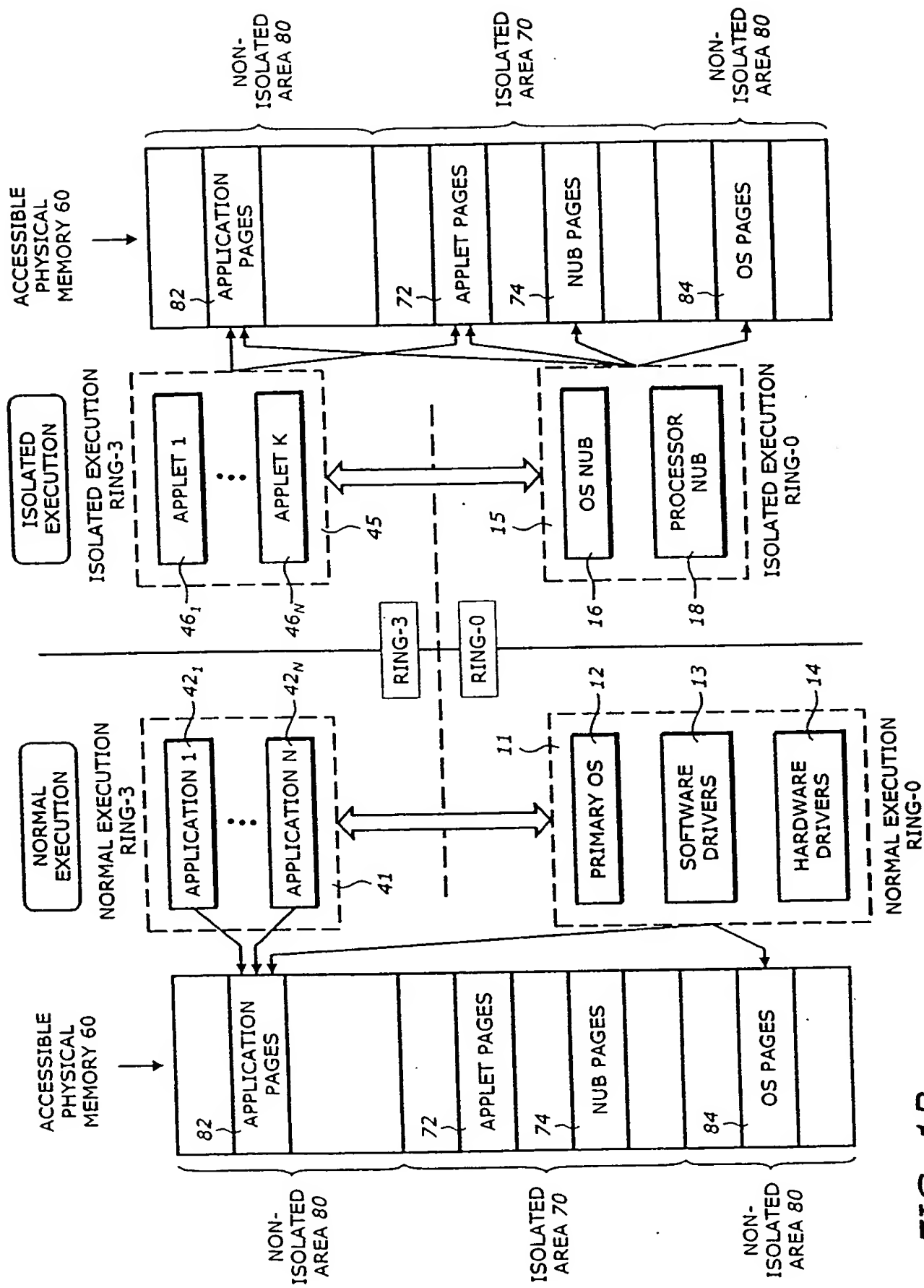


FIG. 1B

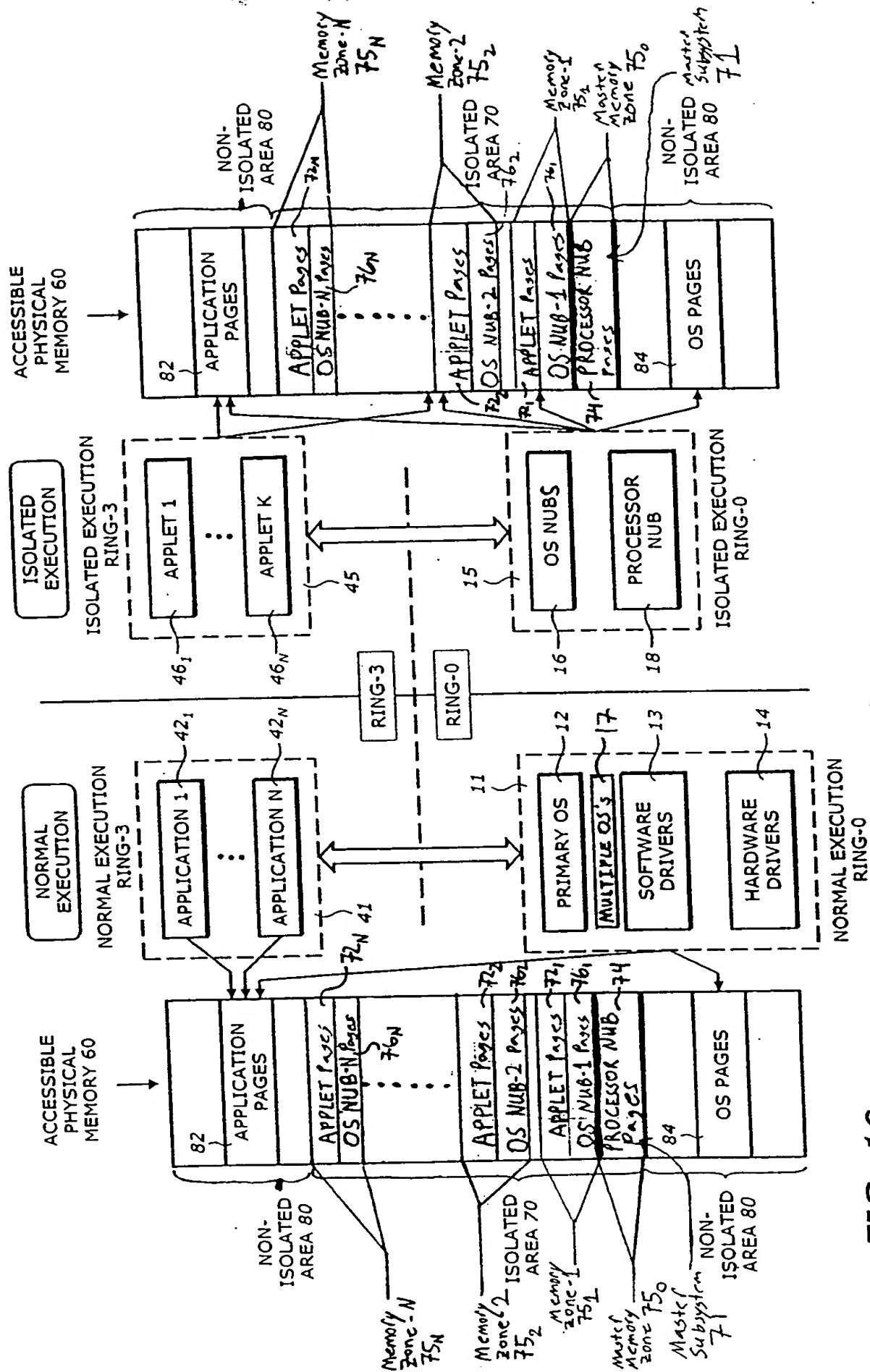


FIG. 1C

100

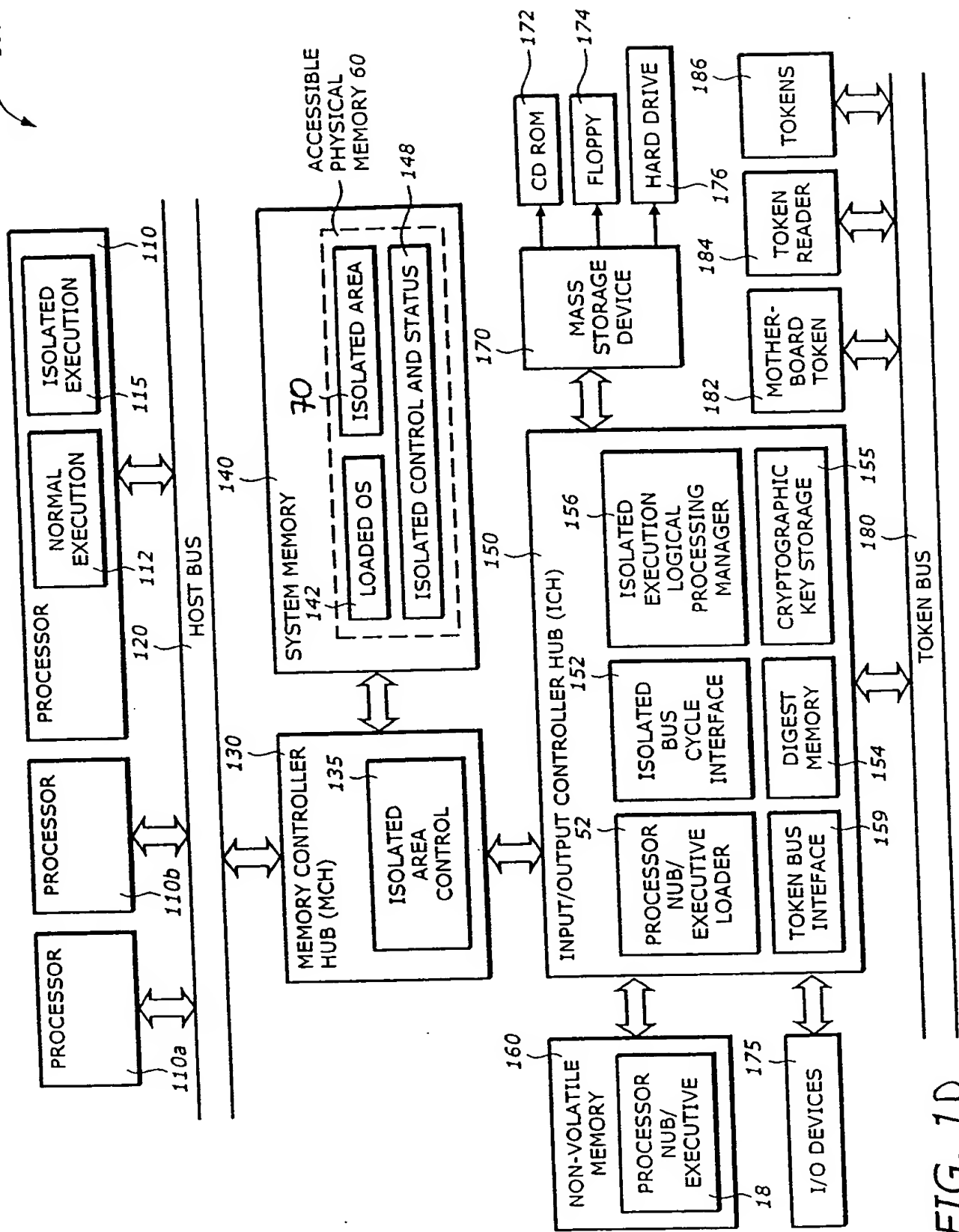


FIG. 10

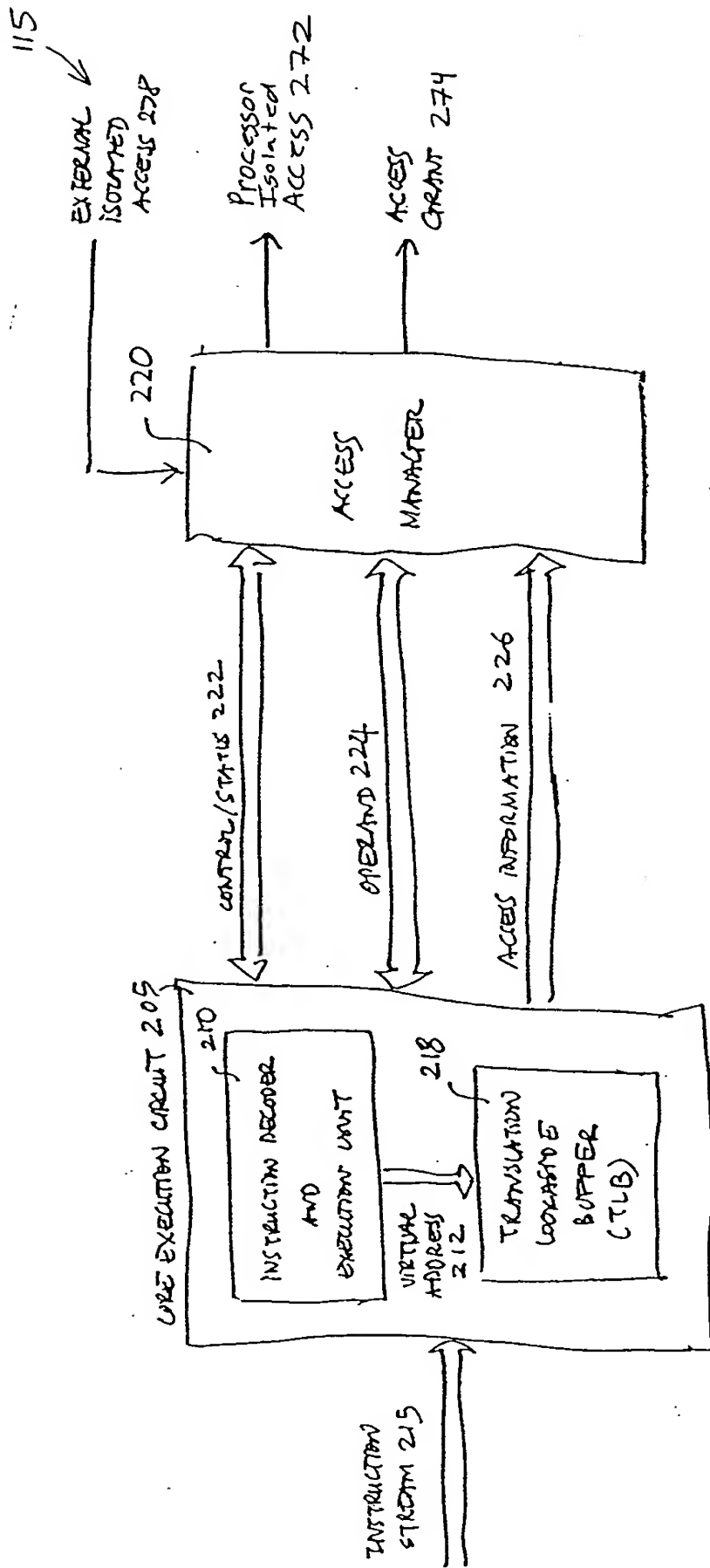


FIG. 2A

EXTERNAL  
FORWARD NUM 278

Configuration Storage 250

Processor Control Register 252

Execution mode word 253

Isolated Setting Register 260

Isolated Setting 268

Mask Register 262

Isolated Mask Value 263

Base Register 264

Isolated Base Value 265

Length Register 266

Isolated Length Value 267

Isolated Subsystem Memory Range Settings Register 280

ID <sub>1</sub>	Entry Attributes	Subsystem Range Setting	Entry Context
ID <sub>2</sub>	Entry Attributes	Subsystem Range Setting	Entry Context

ID <sub>3</sub>	Entry Attributes	Subsystem Range Setting	Entry Context
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283 285 287 288

Current Subsystem Range Setting Configuration 273

Current Entry Context Configuration 275

Entry Frame Register 294

Entry Address 295

Subsystem Mask Register 289

Subsystem Mask Value 290

Subsystem Base Register 291

Subsystem Base Value 292

Length Register 296

Subsystem Length Value 297

270

MULTI-MEMORY  
ZONE  
ACCESS  
CHECKER  
CIRCUIT

Processor  
Isolated  
Access 272

ACCESS  
GRANT 274

PHYSICAL ADDRESS 282

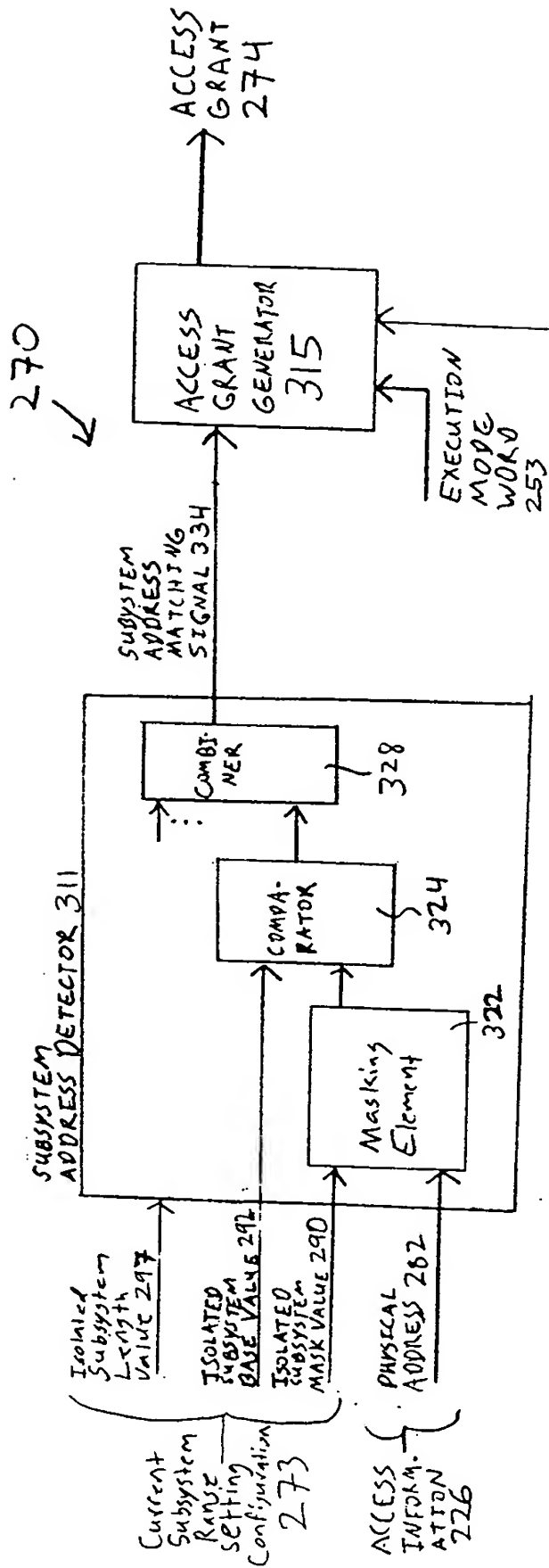
READ / WRITE (R0/WRT) 284

ACCESS TYPE 286

ACCESS  
INFORMATION  
226

Operand  
224

Isolated  
Subsystem  
Memory Range  
Settings 281



Processor Isolated Access Signal 272

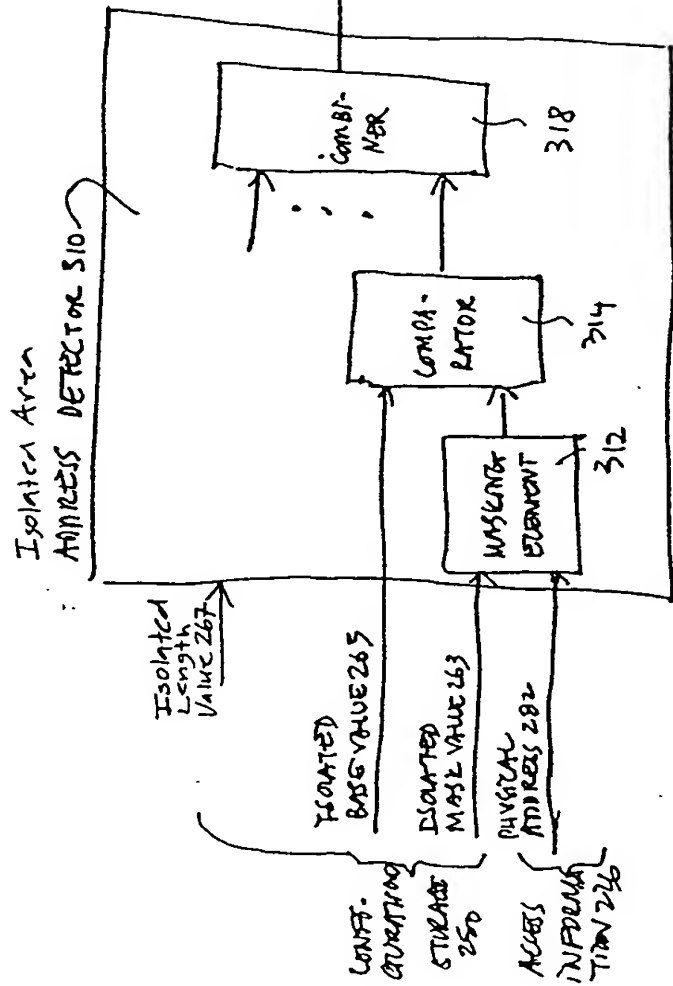


FIG. 3

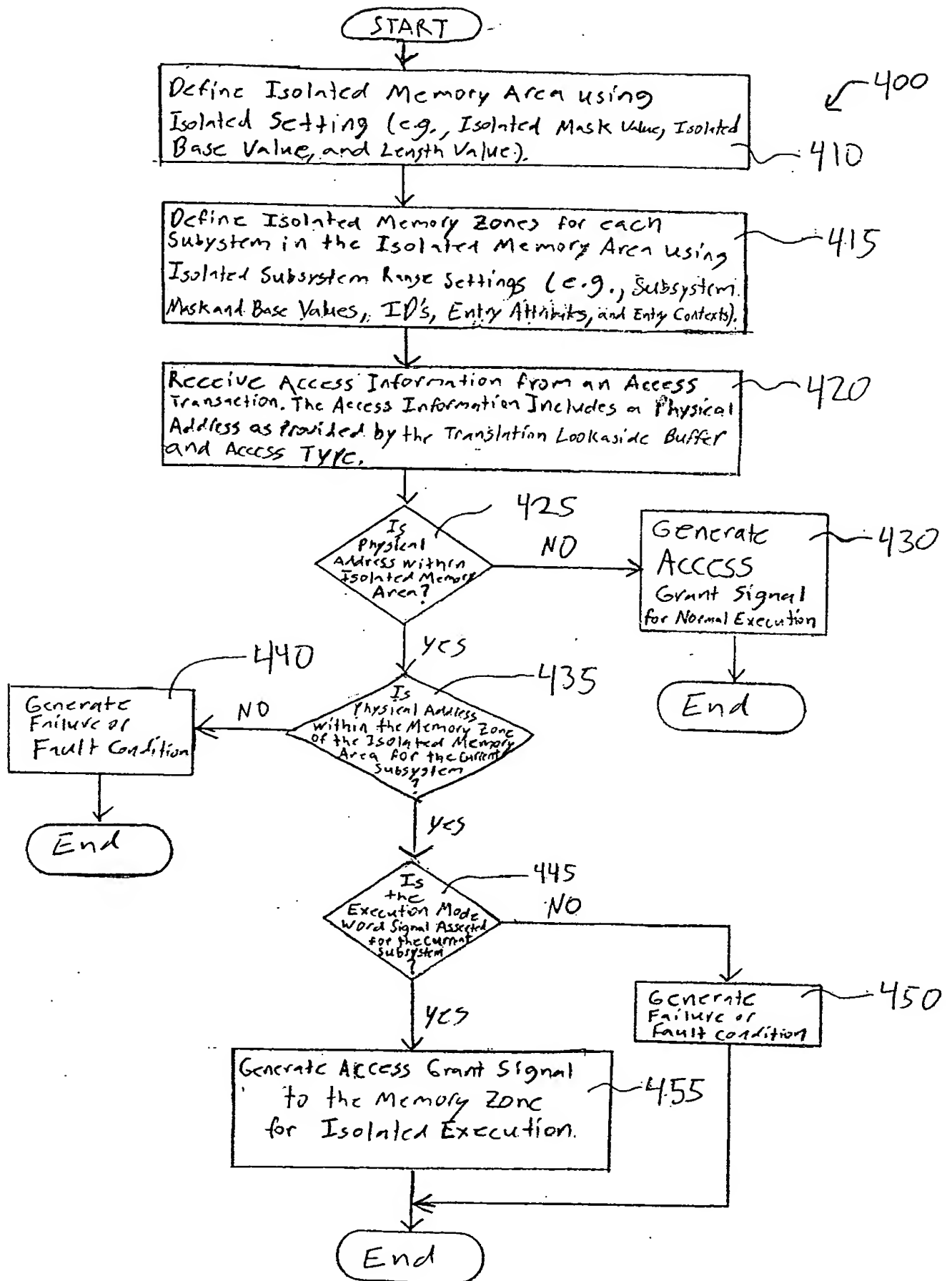


FIG. 4